



COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No: 3101 P R No: 201507401 Sex: M Name: BHASALE VINAY BHANUDAS
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	BB P	
	IA	2	BB P	
Digital VLSI Design	Theory	4	BC P	
	IA	2	BB P	
Analog VLSI Design	Theory	4	CC P	
	IA	2	AB P	
VLSI Technology	Theory	4	BB P	
	IA	2	BB P	
Advanced Engineering Mathematics	Theory	4	FF F	
	IA	2	CC P	
HDL - LAB	IA	2	AB P	
	Practical	2	AA P	
VLSI CAD - LAB	IA	2	BB P	
	Practical	2	BB P	
Total :		38		6.05 F FAILS

Seat No: 3102 P R No: 201107681 Sex: M Name: BORKAR NEEL RAMAKANT
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	BB P	
	IA	2	AB P	
Digital VLSI Design	Theory	4	CC P	
	IA	2	AB P	
Analog VLSI Design	Theory	4	AB P	
	IA	2	AB P	
VLSI Technology	Theory	4	AB P	
	IA	2	AB P	
Advanced Engineering Mathematics	Theory	4	FF F	
	IA	2	BC P	
HDL - LAB	IA	2	AB P	
	Practical	2	BB P	
VLSI CAD - LAB	IA	2	BB P	
	Practical	2	BC P	
Total :		38		6.42 F FAILS

NB/WB/HEICD
A



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3103	P R No : 201108381	Sex : F	Name : GOVEKAR DIVYA DIGAMBAR
No Of Attempts : 1			
	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	CC P	
IA	2	BB P	
Digital VLSI Design			
Theory	4	FF F	
IA	2	CC P	
Analog VLSI Design			
Theory	4	FF F	
IA	2	BB P	
VLSI Technology			
Theory	4	CC P	
IA	2	BC P	
Advanced Engineering Mathematics			
Theory	4	FF F	
IA	2	BC P	
HDL - LAB			
IA	2	AA P	
Practical	2	AB P	
VLSI CAD - LAB			
IA	2	BB P	
Practical	2	BC P	
Total :	38		4.26 F FAILS

Seat No : 3104	P R No : 201006305	Sex : F	Name : KALSHAONKAR REEMA DATTARAM
No Of Attempts : 1			
	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BB P	
IA	2	AA P	
Digital VLSI Design			
Theory	4	BC P	
IA	2	BC P	
Analog VLSI Design			
Theory	4	AA P	
IA	2	AA P	
VLSI Technology			
Theory	4	AA P	
IA	2	AA P	
Advanced Engineering Mathematics			
Theory	4	BC P	
IA	2	AB P	
HDL - LAB			
IA	2	AO P	
Practical	2	AO P	
VLSI CAD - LAB			
IA	2	BB P	
Practical	2	BC P	
Total :	38		7.79 P PASSES



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3105	P R No : 200907827	Sex : F	Name : KHANDEPARKAR SHWETA MAHENDRA	
No Of Attempts : 1				
		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	AB P	
	IA	2	AB P	
Digital VLSI Design				
	Theory	4	AB P	
	IA	2	AB P	
Analog VLSI Design				
	Theory	4	BB P	
	IA	2	AB P	
VLSI Technology				
	Theory	4	BB P	
	IA	2	AB P	
Advanced Engineering Mathematics				
	Theory	4	BB P	
	IA	2	AB P	
HDL - LAB				
	IA	2	AA P	
	Practical	2	AA P	
VLSI CAD - LAB				
	IA	2	AB P	
	Practical	2	BB P	
		Total :	38	7.74 P PASSES

Seat No : 3106	P R No : 201008499	Sex : F	Name : KHARANGATE LAXIMI SHASHANK	
No Of Attempts : 1				
		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	AB P	
	IA	2	AA P	
Digital VLSI Design				
	Theory	4	BC P	
	IA	2	AB P	
Analog VLSI Design				
	Theory	4	AB P	
	IA	2	AB P	
VLSI Technology				
	Theory	4	AB P	
	IA	2	AA P	
Advanced Engineering Mathematics				
	Theory	4	BC P	
	IA	2	BB P	
HDL - LAB				
	IA	2	AA P	
	Practical	2	AB P	
VLSI CAD - LAB				
	IA	2	AB P	
	Practical	2	AB P	
		Total :	38	7.68 P PASSES



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No: 3107 P R No: 201105539 Sex: M Name: KORDE CHARUDATTA GURUDAS
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BC P	
IA	2	AB P	
Digital VLSI Design			
Theory	4	BC P	
IA	2	AB P	
Analog VLSI Design			
Theory	4	BC P	
IA	2	AA P	
VLSI Technology			
Theory	4	BC P	
IA	2	BB P	
Advanced Engineering Mathematics			
Theory	4	CC P	
IA	2	BC P	
HDL - LAB			
IA	2	AA P	
Practical	2	AO P	
VLSI CAD - LAB			
IA	2	AA P	
Practical	2	AA P	
Total :	38		7.00 P PASSES

Seat No: 3108 P R No: 201107707 Sex: M Name: MAHATME MOHIT BHAIKAV
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BC P	
IA	2	BB P	
Digital VLSI Design			
Theory	4	CC P	
IA	2	CC P	
Analog VLSI Design			
Theory	4	BC P	
IA	2	BC P	
VLSI Technology			
Theory	4	FF F	
IA	2	BB P	
Advanced Engineering Mathematics			
Theory	4	CC P	
IA	2	BC P	
HDL - LAB			
IA	2	AA P	
Practical	2	AB P	
VLSI CAD - LAB			
IA	2	AB P	
Practical	2	BB P	
Total :	38		5.63 F FAILS

AI



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3109 P R No : 201007353 Sex : F Name : MAYEKAR MITHILI NARAYAN
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BC P	
IA	2	BC P	
Digital VLSI Design			
Theory	4	CC P	
IA	2	CC P	
Analog VLSI Design			
Theory	4	FF F	
IA	2	BC P	
VLSI Technology			
Theory	4	CC P	
IA	2	CC P	
Advanced Engineering Mathematics			
Theory	4	FF F	
IA	2	CC P	
HDL - LAB			
IA	2	AB P	
Practical	2	BB P	
VLSI CAD - LAB			
IA	2	BC P	
Practical	2	CC P	
Total :	38		4.47 F FAILS

Seat No : 3110 P R No : 201106308 Sex : F Name : NAIK STHRIGDHARA NAVANATH
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	BC P	
IA	2	AB P	
Digital VLSI Design			
Theory	4	AB P	
IA	2	BC P	
Analog VLSI Design			
Theory	4	CC P	
IA	2	BB P	
VLSI Technology			
Theory	4	BC P	
IA	2	BB P	
Advanced Engineering Mathematics			
Theory	4	FF F	
IA	2	CC P	
HDL - LAB			
IA	2	BB P	
Practical	2	BB P	
VLSI CAD - LAB			
IA	2	BB P	
Practical	2	BC P	
Total :	38		5.79 F FAILS



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3111	P R No : 201008518	Sex : F	Name : NAIK TEJA UDAY	
No Of Attempts : 1		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	AB P	
	IA	2	AB P	
Digital VLSI Design				
	Theory	4	BC P	
	IA	2	BB P	
Analog VLSI Design				
	Theory	4	BB P	
	IA	2	BB P	
VLSI Technology				
	Theory	4	AB P	
	IA	2	AA P	
Advanced Engineering Mathematics				
	Theory	4	CC P	
	IA	2	CC P	
HDL - LAB				
	IA	2	BB P	
	Practical	2	BC P	
VLSI CAD - LAB				
	IA	2	BB P	
	Practical	2	BC P	
Total :		38	6.84 P PASSES	

Seat No : 3112	P R No : 201107719	Sex : M	Name : PATIL ABHIJEET VILAS	
No Of Attempts : 1		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	FF F	
	IA	2	BC P	
Digital VLSI Design				
	Theory	4	FF F	
	IA	2	CC P	
Analog VLSI Design				
	Theory	4	FF F	
	IA	2	BC P	
VLSI Technology				
	Theory	4	CC P	
	IA	2	CC P	
Advanced Engineering Mathematics				
	Theory	4	FF F	
	IA	2	CC P	
HDL - LAB				
	IA	2	AB P	
	Practical	2	BB P	
VLSI CAD - LAB				
	IA	2	BB P	
	Practical	2	CC P	
Total :		38	3.37 F FAILS	



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3113	P R No : 201105565	Sex : M	Name : RANE KARAN PARSHURAM	
No Of Attempts : 1		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	BB P	
	IA	2	AB P	
Digital VLSI Design				
	Theory	4	CC P	
	IA	2	BB P	
Analog VLSI Design				
	Theory	4	AA P	
	IA	2	AO P	
VLSI Technology				
	Theory	4	AB P	
	IA	2	BB P	
Advanced Engineering Mathematics				
	Theory	4	BB P	
	IA	2	AB P	
HDL - LAB				
	IA	2	AA P	
VLSI CAD - LAB				
	Practical	2	AA P	
	IA	2	AA P	
	Practical	2	AA P	
Total :		38	7.79 P PASSES	

Seat No : 3114	P R No : 201106312	Sex : M	Name : SAWAL SAGAR NAMDEV	
No Of Attempts : 1		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	BB P	
	IA	2	BB P	
Digital VLSI Design				
	Theory	4	BB P	
	IA	2	BC P	
Analog VLSI Design				
	Theory	4	BC P	
	IA	2	BB P	
VLSI Technology				
	Theory	4	BB P	
	IA	2	BC P	
Advanced Engineering Mathematics				
	Theory	4	BC P	
	IA	2	CC P	
HDL - LAB				
	IA	2	AB P	
VLSI CAD - LAB				
	Practical	2	AB P	
	IA	2	AA P	
	Practical	2	AB P	
Total :		38	6.84 P PASSES	



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3115	P R No : 201107394	Sex : M	Name : SHETGAONKAR ANAY ANIL	
No Of Attempts : 1		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	FF F	
	IA	2	BC P	
Digital VLSI Design				
	Theory	4	FF F	
	IA	2	CC P	
Analog VLSI Design				
	Theory	4	FF F	
	IA	2	CC P	
VLSI Technology				
	Theory	4	FF F	
	IA	2	CC P	
Advanced Engineering Mathematics				
	Theory	4	FF F	
	IA	2	CC P	
HDL - LAB				
	IA	2	BB P	
	Practical	2	BB P	
VLSI CAD - LAB				
	IA	2	BB P	
	Practical	2	BC P	
Total :		38	2.79 F FAILS	

Seat No : 3116	P R No : 200806775	Sex : M	Name : ESTEBEIRO SHELTON PIETY	
No Of Attempts : 4		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	CC +	
	IA	2	CC +	
Digital VLSI Design				
	Theory	4	CC +	
	IA	2	BB +	
Analog VLSI Design				
	Theory	4	CC +	
	IA	2	CC +	
VLSI Technology				
	Theory	4	BB +	
	IA	2	BB +	
Advanced Engineering Mathematics				
	Theory	4	CC P	
	IA	2	BC +	
HDL - LAB				
	IA	2	BB +	
	Practical	2	AB +	
VLSI CAD - LAB				
	IA	2	BC +	
	Practical	2	AB +	
Total :		38	5.95 P PASSES	



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No :	P R No :	Sex :	Name :	No Of Credits	Grade Obtained	SGPA
3117	201004714	F	KANADE SHRADDHA SABAJI			
No Of Attempts : 2						
Solid State Devices and Semiconductor Physics						
				Theory	4	CC +
				IA	2	AA +
Digital VLSI Design						
				Theory	4	BC +
				IA	2	BB +
Analog VLSI Design						
				Theory	4	BC +
				IA	2	BB +
VLSI Technology						
				Theory	4	CC +
				IA	2	AB +
Advanced Engineering Mathematics						
				Theory	4	FF F
				IA	2	CC +
HDL - LAB						
				IA	2	AA +
				Practical	2	AA +
VLSI CAD - LAB						
				IA	2	AB +
				Practical	2	AB +
				Total :	38	6.00 F FAILS

Seat No :	P R No :	Sex :	Name :	No Of Credits	Grade Obtained	SGPA
3118	201301154	M	NAIK MILIND MINANATH			
No Of Attempts : 3						
Solid State Devices and Semiconductor Physics						
				Theory	4	BC +
				IA	2	CC +
Digital VLSI Design						
				Theory	4	CC +
				IA	2	AB +
Analog VLSI Design						
				Theory	4	CC +
				IA	2	CC +
VLSI Technology						
				Theory	4	CC +
				IA	2	CC +
Advanced Engineering Mathematics						
				Theory	4	FF F
				IA	2	CC +
HDL - LAB						
				IA	2	CC +
				Practical	2	CC +
VLSI CAD - LAB						
				IA	2	CC +
				Practical	2	AB +
				Total :	38	4.89 F FAILS



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3119	P R No : 200800872	Sex : M	Name : NORONHA ALWIN	
No Of Attempts : 2		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	CC P	
	IA	2	BB +	
Digital VLSI Design				
	Theory	4	FF F	
	IA	2	BB +	
Analog VLSI Design				
	Theory	4	CC P	
	IA	2	BB +	
VLSI Technology				
	Theory	4	CC P	
	IA	2	AB +	
Advanced Engineering Mathematics				
	Theory	4	FF F	
	IA	2	CC +	
HDL - LAB				
	IA	2	BB +	
	Practical	2	BC +	
VLSI CAD - LAB				
	IA	2	AB +	
	Practical	2	AB +	
Total :		38		4.89 F FAILS

Seat No : 3120	P R No : 200806910	Sex : M	Name : SATARDEKAR GIRISH GURUDAS	
No Of Attempts : 2		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	BC +	
	IA	2	BC +	
Digital VLSI Design				
	Theory	4	BB +	
	IA	2	BB +	
Analog VLSI Design				
	Theory	4	BC +	
	IA	2	BB +	
VLSI Technology				
	Theory	4	BC P	
	IA	2	AB +	
Advanced Engineering Mathematics				
	Theory	4	FF F	
	IA	2	CC +	
HDL - LAB				
	IA	2	BC +	
	Practical	2	BC +	
VLSI CAD - LAB				
	IA	2	AB +	
	Practical	2	BB +	
Total :		38		5.79 F FAILS



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3121 P R No : 201006303 Sex : M Name : TRAVASSO VALERIUS SIRUS
No Of Attempts : 2

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	CC +	
IA	2	BB +	
Digital VLSI Design			
Theory	4	AA +	
IA	2	AB +	
Analog VLSI Design			
Theory	4	BC +	
IA	2	AB +	
VLSI Technology			
Theory	4	CC +	
IA	2	AB +	
Advanced Engineering Mathematics			
Theory	4	BC P	
IA	2	AB +	
HDL - LAB			
IA	2	AB +	
Practical	2	AA +	
VLSI CAD - LAB			
IA	2	AB +	
Practical	2	AB +	
Total :	38		7.05 P PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : *[Signature]*

Checked By : *[Signature]*

Date : 24/2/16

[Signature]
24/2/2016
S.S.J. Figueiredo
Assistant Registrar-E(Prof.)

[Signature]
24/2/16
Leo V. Macedo
Controller Of Examinations

[Signature]
24/2/16
Prof. V.P. Kamat
Registrar

